

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Watts

Filed: Herewith

Serial No.: TBD

For: Parallel Integrated Circuit Test Apparatus and Test Method

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Art Unit: TBD

INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL

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U.S. Patent and Trademark Office
P.O. Box 2327
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MAILING CERTIFICATE PURSUANT
TO 37 C.F.R. §1.10

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Joan K. Konkel
Joan K. Konkel

12-21-01
Date

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the
information noted on the enclosed PTO-1449 which may be considered material to the
examination of the above identified application. This Information Disclosure Statement is
submitted pursuant to 37 CFR 1.97(b) concurrently with the filing of the patent application.
Consideration of the references is respectfully requested.

Respectfully submitted,

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